

# MM300HF170T2NH

## SiC MOSFET Module

Preliminary Data

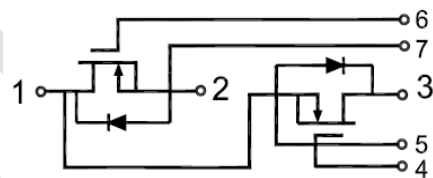
### Features:

- Ultra Low Loss
- High-Frequency Operation
- Zero Reverse Recovery Current from Diode
- Zero Turn-off Tail Current from MOSFET
- Normally-off, Fail-safe Device Operation
- Easy of Paralleling
- Copper Baseplate and Aluminum Nitride Insulator



### Applications:

- HF Resonant Converters/Inverters
- Motor Drivers
- Solar and Wind Inverters
- UPS and SMPS
- Traction



### Absolute Maximum Ratings ( $T_C=25^{\circ}\text{C}$ unless otherwise specified)

Symbol	Description		Value	Units
$V_{DSmax}$	Drain-Source Voltage		1700	V
$V_{GSmax}$	Gate-Source Voltage	Absolute Maximum values	-10/+25	V
$V_{GSop}$	Gate-Source Voltage	Recommended Operational Values	-5/20	V
$I_D$	Continuous Drain Current	$V_{GS}=20V, T_C=25^{\circ}\text{C}$	325	A
		$V_{GS}=20V, T_C=90^{\circ}\text{C}$	225	A
$I_{D(pluse)}$	Pulsed Drain Current	Pulse width $t_p=200\mu\text{s}$ Repetition rate limited by $T_{jmax}, T_C=25^{\circ}\text{C}$	900	A
$P_D$	Power Dissipation	$T_C=25^{\circ}\text{C}, T_J=150^{\circ}\text{C}$	1760	W

### Electrical Characteristics of MOSFET ( $T_C=25^{\circ}\text{C}$ unless otherwise specified)

Symbol	Description	Conditions	Min	Typ	Max	Unit
$V_{(BR)DSS}$	Drain - Source Breakdown Voltage	$V_{GS}=0V, I_D=1mA$	1.7			V
$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS} = 10\text{ V}, I_D = 15\text{ mA}$	1.8	2.3		V
$I_{DSS}$	Zero Gate Voltage Drain Current	$V_{DS} = 1.7\text{ kV}, V_{GS} = 0V$		500	1000	$\mu A$
		$V_{DS} = 1.7\text{ kV}, V_{GS} = 0V, T_J = 150^{\circ}\text{C}$		1500	3000	$\mu A$
$I_{GSS}$	Gate-Source Leakage Current	$V_{GS} = 20\text{ V}, V_{DS} = 0V$		1	600	nA
$R_{DS(on)}$	On State Resistance	$V_{GS} = 20\text{ V}, I_{DS} = 225A$		8.0	10.0	m $\Omega$
		$V_{GS} = 20\text{ V}, I_{DS} = 225\text{ A}, T_J = 150^{\circ}\text{C}$		16.2	20	
$g_{fs}$	Transconductance	$V_{DS}= 20\text{ V}, I_{DS} = 225\text{ A}$		95		S
		$V_{DS}= 20\text{ V}, I_{DS} = 225\text{ A}, T_J=150^{\circ}\text{C}$		82		
$C_{iss}$	Input Capacitance	$V_{DS} = 1k\text{ V}, f = 200\text{ kHz}, V_{AC} = 25\text{ mV}$		20		nF
$C_{OSS}$	Output Capacitance			2.5		
$C_{rss}$	Reverse Transfer Capacitance			0.08		
$E_{on}$	Turn-On Switching Energy	$V_{DD} = 900\text{ V}, V_{GS} = -5V/+20V, I_D = 300\text{ A}, R_{G(ext)} = 2.5\text{ }\Omega$		13.0		mJ
$E_{off}$	Turn-Off Switching Energy			10.0		
$R_{G(int)}$	Internal Gate Resistance	$f = 200\text{ kHz}, V_{AC} = 25\text{ mV}$		3.7		$\Omega$
$Q_{GS}$	Gate-Source Charge	$V_{DD}= 900\text{ V}, V_{GS} = -5V/+20V, I_D= 300\text{ A}, R_{G(ext)} = 2.5\text{ }\Omega$		273		nC
$Q_{GD}$	Gate-Drain Charge			324		
$Q_G$	Total Gate Charge			1076		
$t_{d(on)}$	Turn-off delay time	$V_{DD} = 900V, V_{GS} = -5/+20V, I_D = 300\text{ A}, R_{G(ext)} = 2.5\text{ }\Omega,$		105		ns
$t_r$	Rise Time			72		
$t_{d(off)}$	Turn-off delay time			211		
$t_f$	Fall Time			56		
$R_{\theta JCM}$	Thermal Resistance Junction-To-Case for MOSFET	$T_C=90^{\circ}\text{C}, P_D=150W$		0.067	0.071	$^{\circ}\text{C/W}$

### Free-Wheeling SiC Schottky Diode Characteristics ( $T_C=25^{\circ}\text{C}$ unless otherwise specified)

Symbol	Description	Conditions	Min	Typ	Max	Unit
$V_{SD}$	Diode Forward Voltage	$I_F = 300\text{ A}, V_{GS} = 0$		1.7	2.0	V
		$I_F = 300\text{ A}, V_{GS} = 0, T_J=150^{\circ}\text{C}$		2.2	2.5	
$Q_C$	Total Capacitive Charge			4.4		$\mu\text{C}$
$R_{\theta JCD}$	Thermal Resistance Junction-To-Case for Diode	$T_C=90^{\circ}\text{C}, P_D=130\text{W}$		0.060	0.065	$^{\circ}\text{C/W}$

### Module

Symbol	Description	Conditions	Min	Typ	Max	Unit
$T_{Jmax}$	Junction Temperature		-40		150	$^{\circ}\text{C}$
$T_C, T_{STG}$	Case and Storage Temperature Range		-40		125	$^{\circ}\text{C}$
$V_{isol}$	Case Isolation Voltage	AC, 50 HZ, 1 min	4.5			KV
$L_{stray}$	Stray Inductance	Measured between terminals 2 and 3			14	nH
G	Weight			300		g
M	Mounting Torque	To heatsink and terminal			5	N·m
	Clearance Distance	Terminal to terminal			9	mm
	Creepage Distance	Terminal to terminal			30	mm
		Terminal to baseplate			40	mm

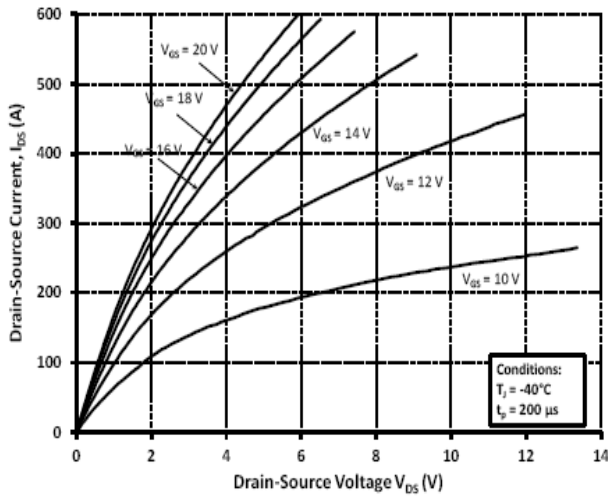


Fig.1 Typical Output Characteristic  $T_j = -40^\circ\text{C}$

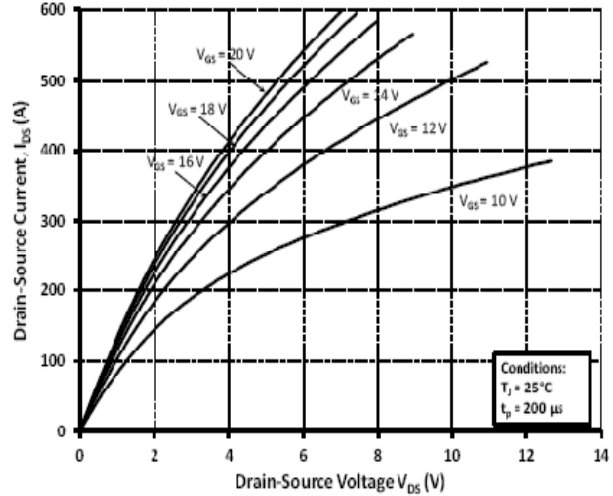


Fig.2 Typical Output Characteristics  $T_j = 25^\circ\text{C}$

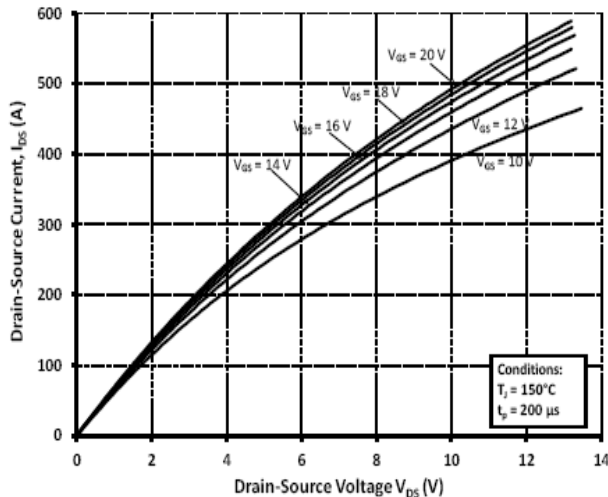


Fig.3 Typical Output Characteristic  $T_j = 150^\circ\text{C}$

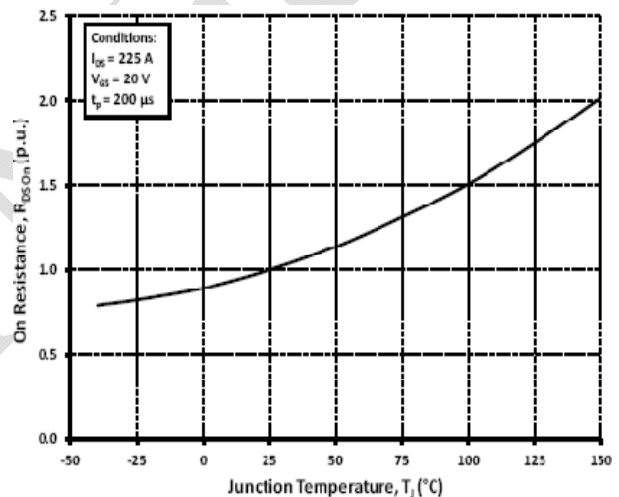


Fig.4 Normalized On-Resistance VS Temperature

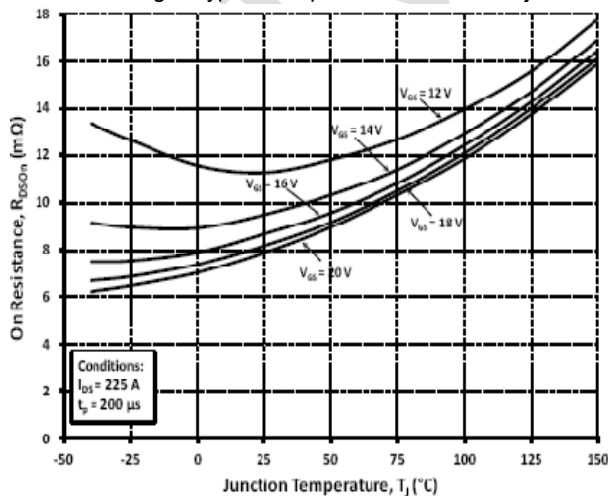


Fig.5 Typical On-Resistance VS Temperature for Various Gate-Source Voltage

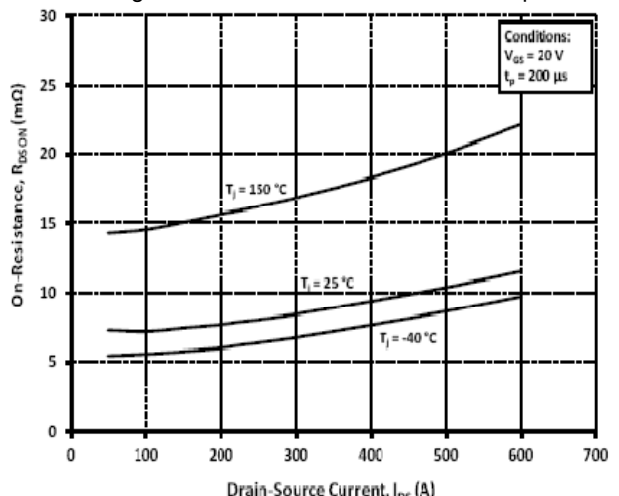


Fig.6 Typical On-Resistance vs. Drain Current For Various Temperature

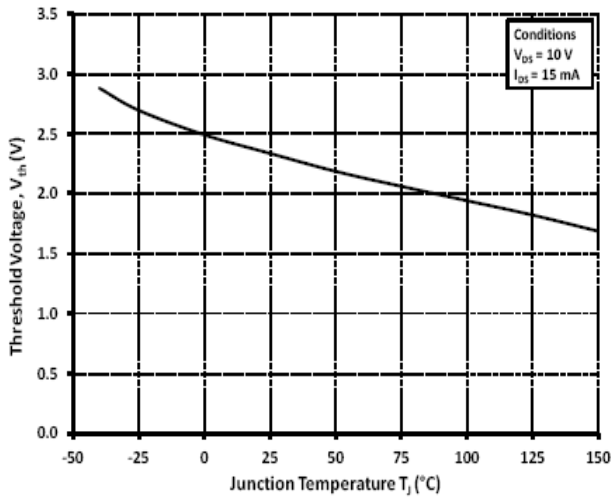


Fig.7 Threshold Voltage VS Temperature

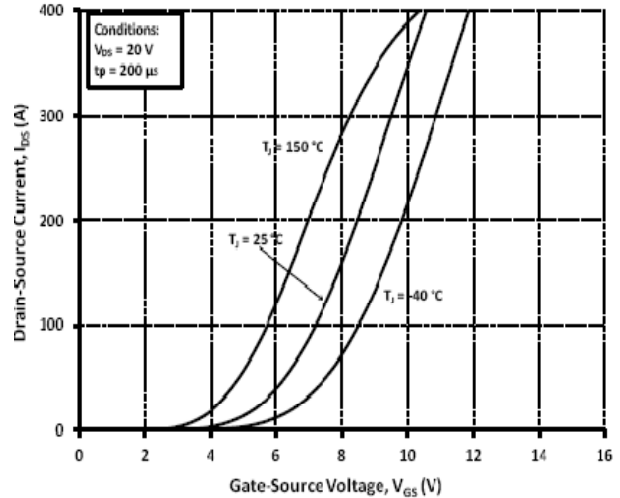


Fig.8 Transfer Characteristic for Various Junction Temperature

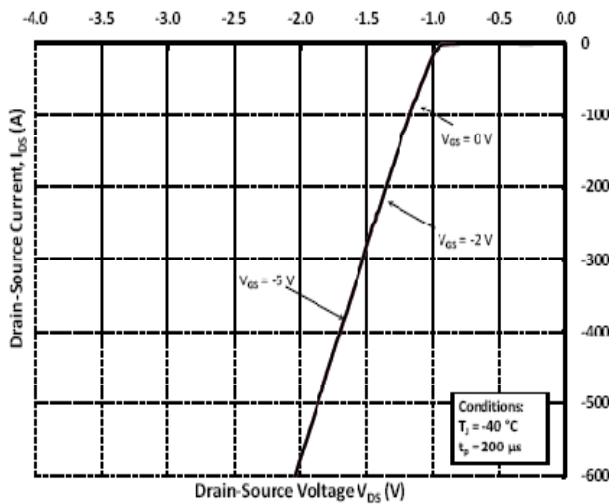


Fig.9 Diode Characteristic at -40°C

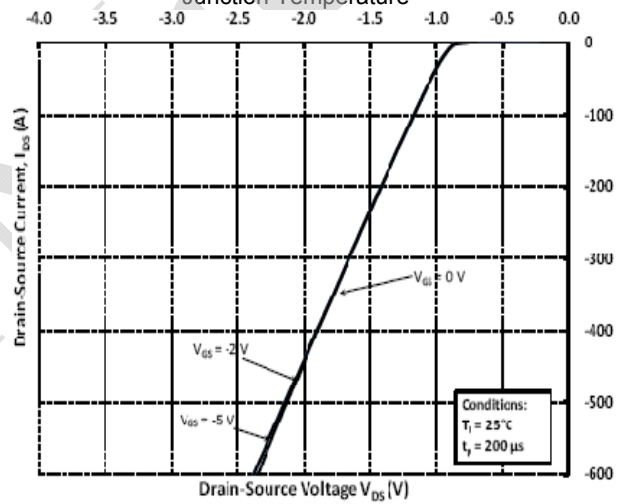


Fig.10 Diode Characteristic at 25°C

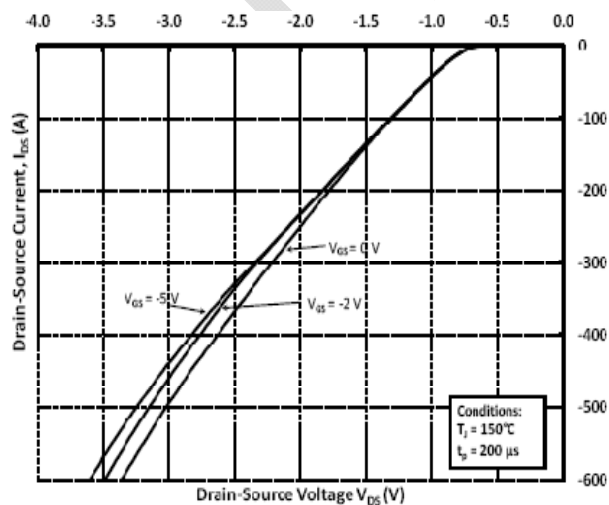


Fig.11 Diode Characteristic at 150°C

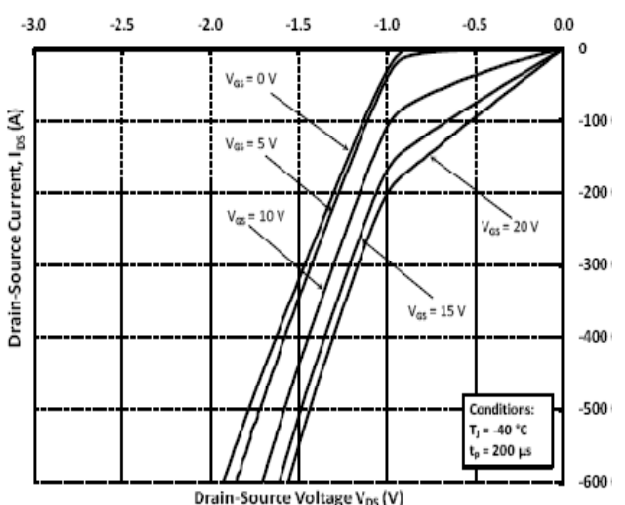


Fig.12 3<sup>rd</sup> Quadrant Characteristic at -40°C

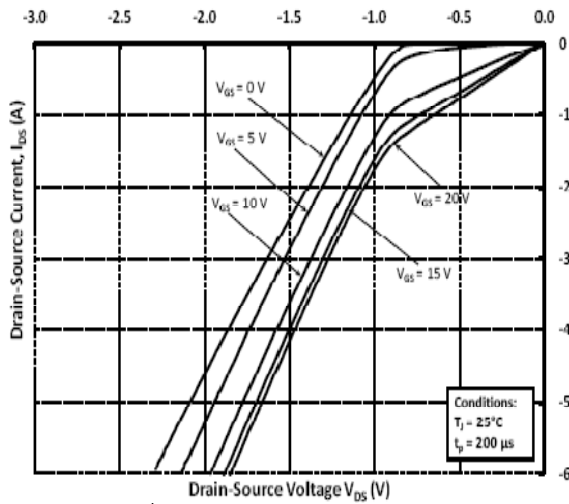


Fig. 13 3<sup>rd</sup> Quadrant Characteristic at 25°C

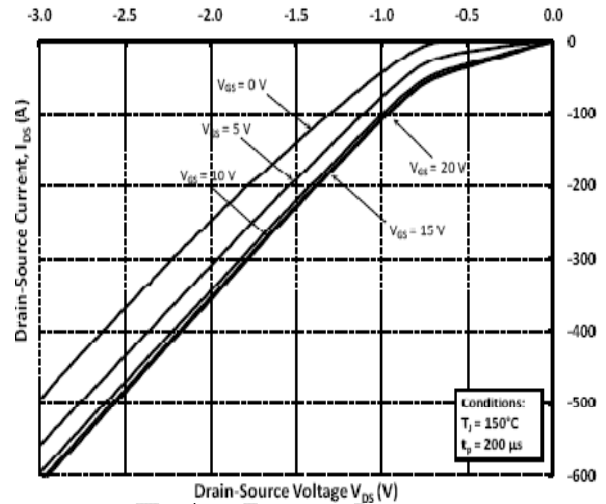


Fig. 14 3<sup>rd</sup> Quadrant Characteristic at 150°C

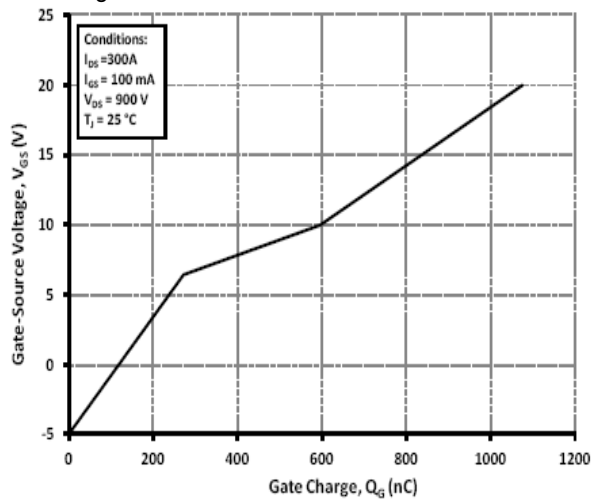


Fig. 15 Typical Gate Charge Characteristic

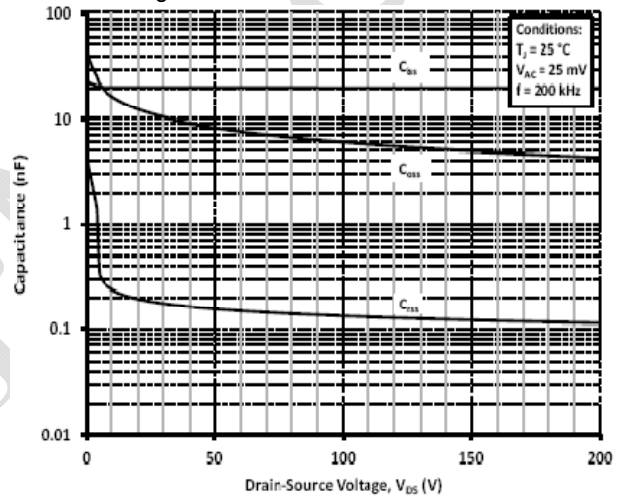


Fig. 16 Typical Capacitances VS Drain-Source Voltage (0-200V)

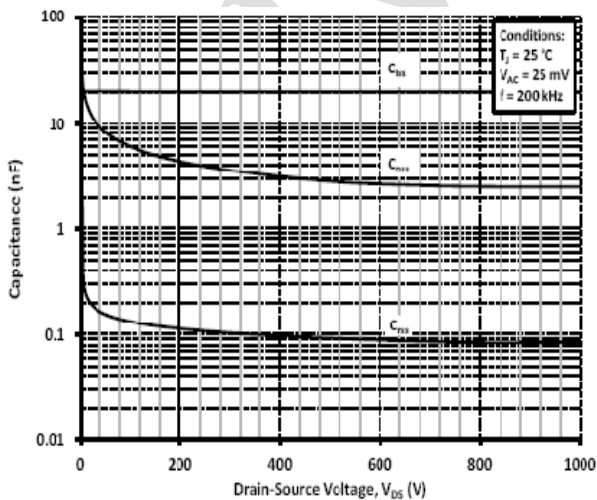


Fig. 17 Typical Capacitances VS Drain-Source Voltage (0-1KV)

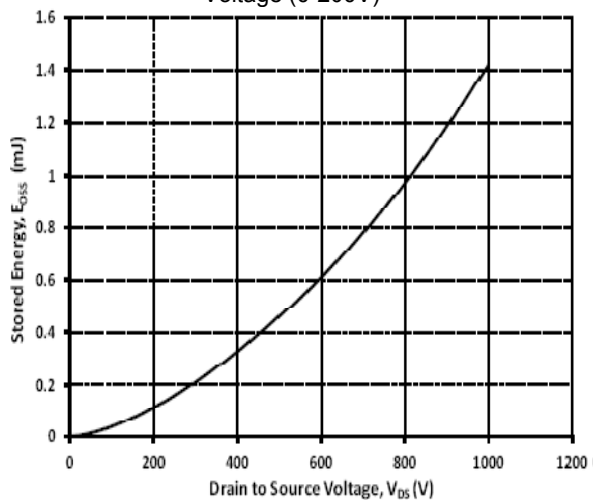


Fig. 18 Typical Output Capacitor Stored Energy

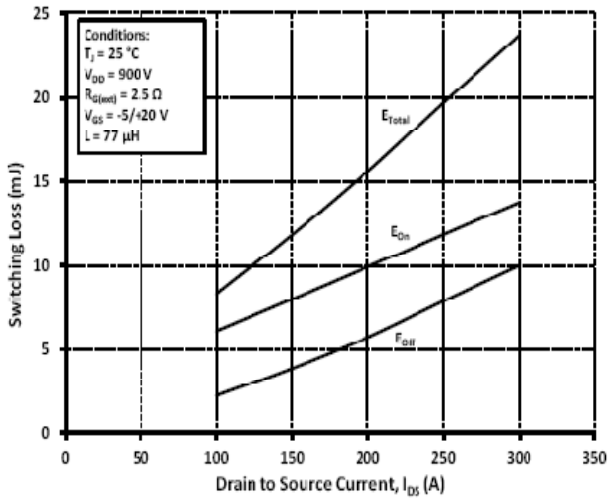


Fig.19 Inductive Switching Energy VS Drain Current For  $V_{DS}=900\text{V}$ ,  $R_G=2.5\ \Omega$

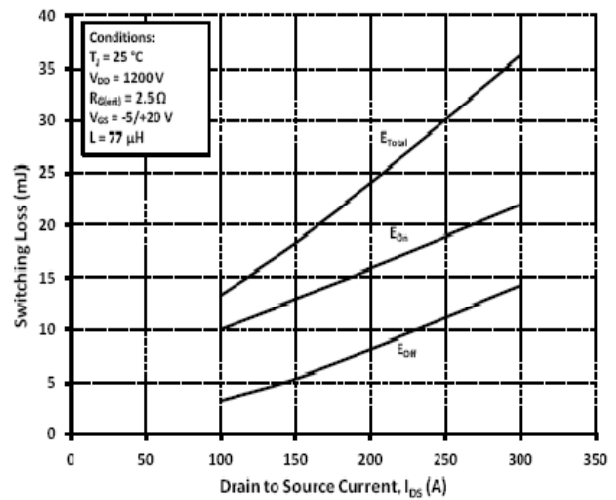


Fig.20 Inductive Switching Energy VS Drain Current For  $V_{DS}=1200\text{V}$ ,  $R_G=2.5\ \Omega$

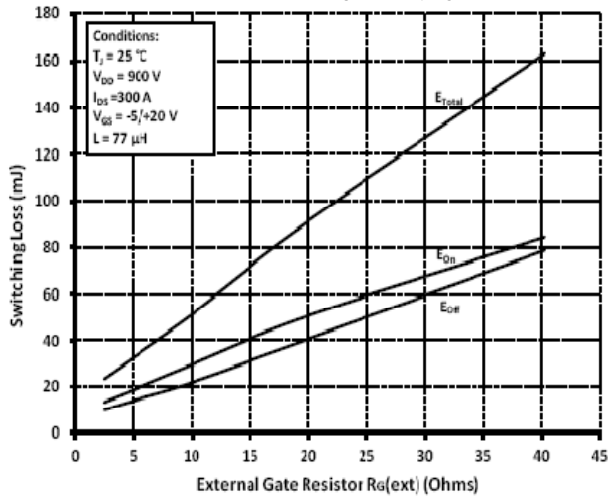


Fig.21 Inductive Switching Energy VS  $R_{G(ext)}$

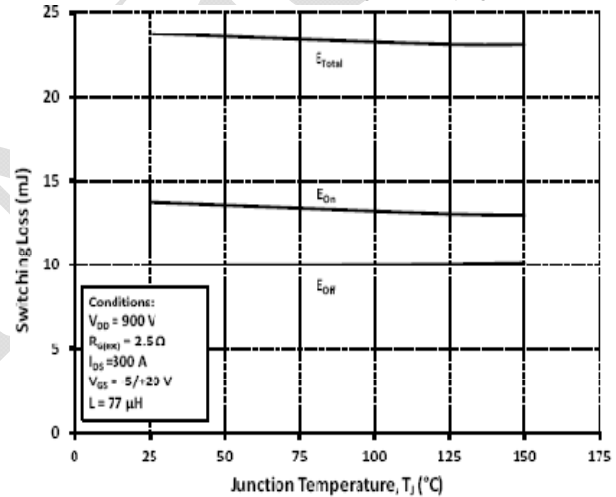


Fig.22 Inductive Switching Energy VS Temperature

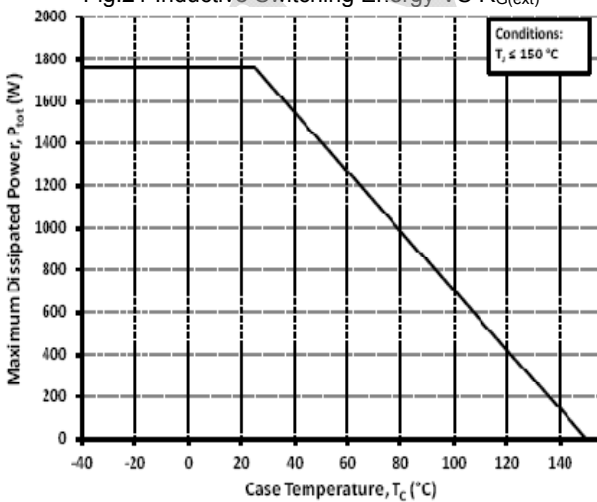


Fig.23 Maximum Power Dissipation (MOSFET) Derating VS Case Temperature

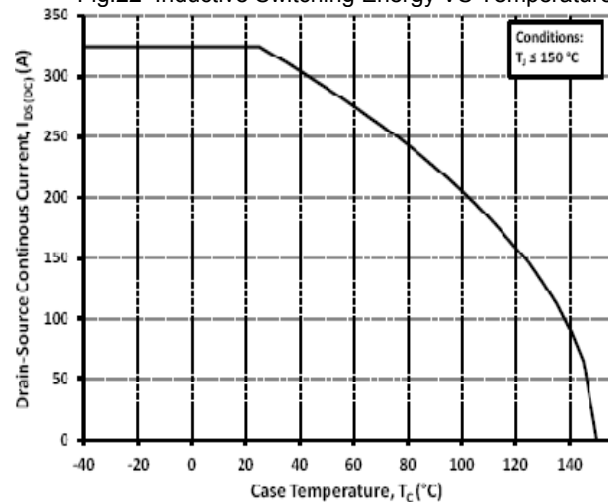


Fig.24 Continuous Drain Current Derating VS Case Temperature



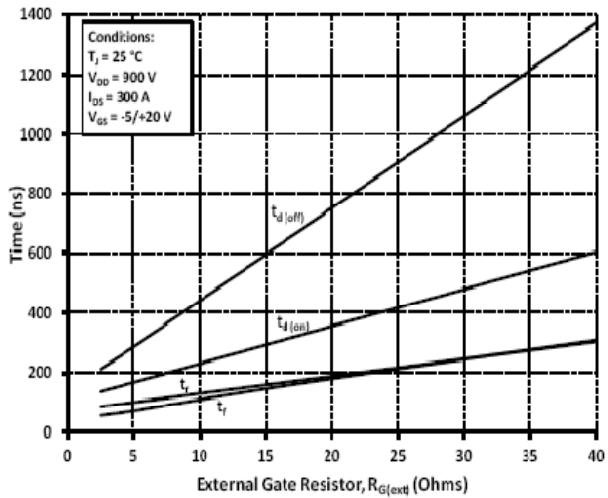


Fig.25 Timing VS  $R_{G(ext)}$

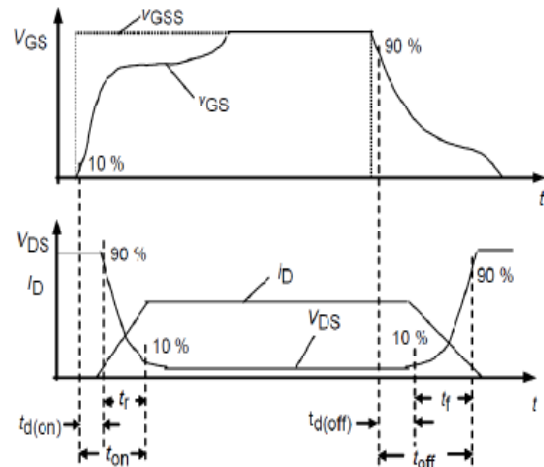


Fig.26 Resistive Switching Time Description

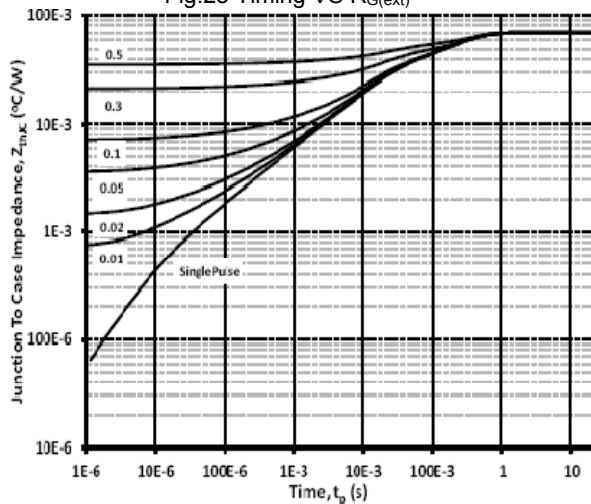


Fig.27 MOSFET Junction Case Thermal Impedance

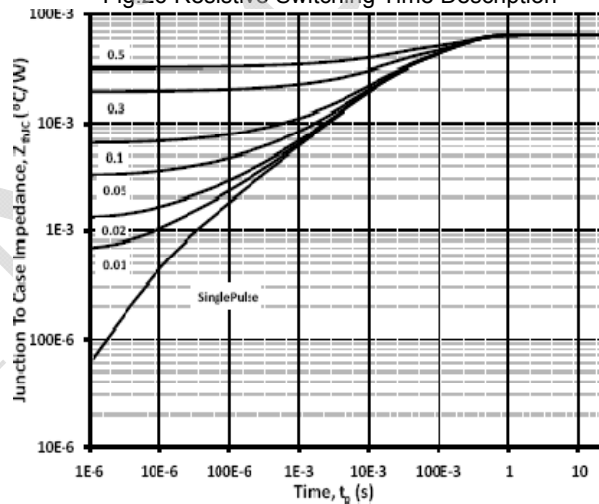


Fig.28 Diode Junction to Case Thermal Impedance

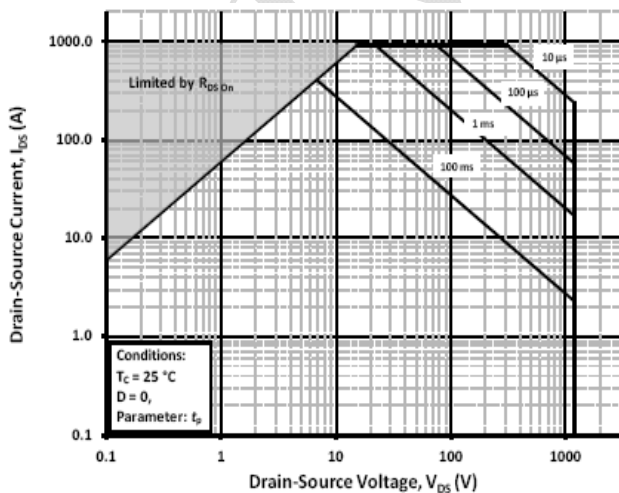
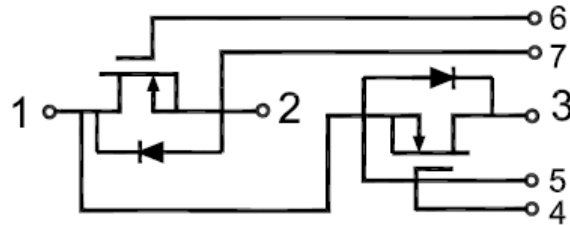


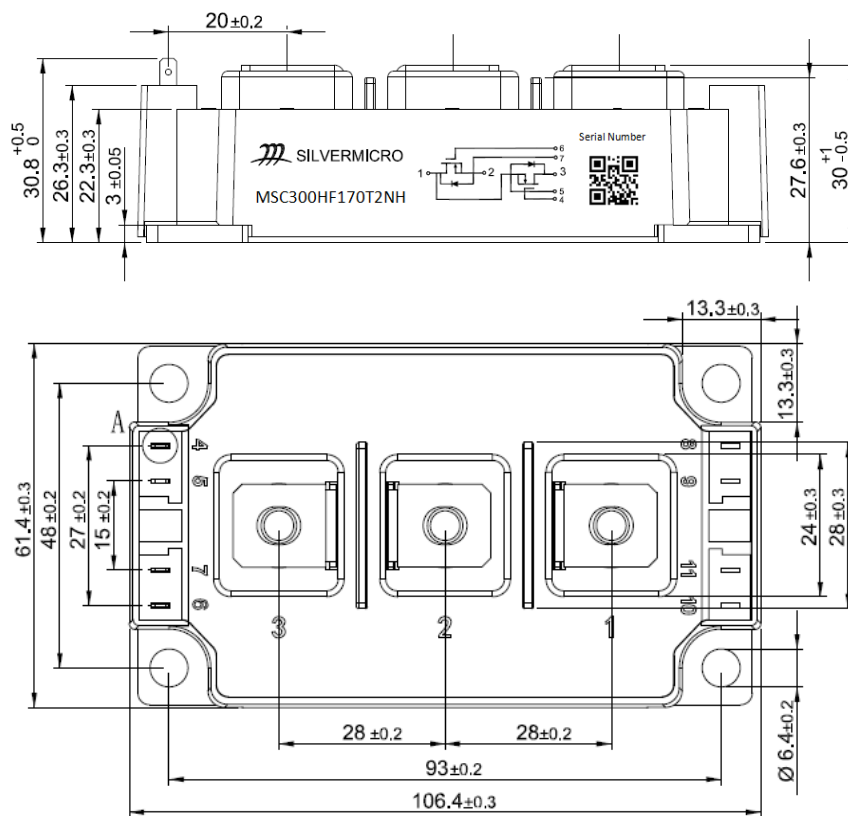
Fig. 29 Maximum Power Dissipation (MOSFET)  
Derating vs Case Temperature



## Internal Circuit



**Package Outline (Unit: mm):**





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