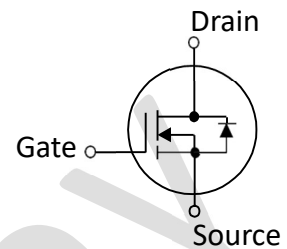


85V, 120A ⁽¹⁾ N-Channel MOSFET

SYMBOL



- Advanced Trench Device Design and Processes
- High Reliability Capability
- 100% CP Probing and Inking

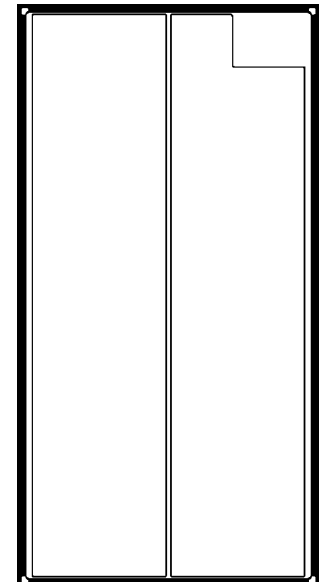
Electrical Characteristics in C/P Test (T_J at 25 °C)

Symbol	Parameter	Min.	Typ.	Max.	Unit	Test Condition
V _{(BR)DSS}	Drain-Source Breakdown Voltage	85	—	—	V	V _{GS} = 0V, I _D = 250μA
R _{DS(ON)}	Static Drain-Source On-Resistance	—	—	6.6	mΩ	V _{GS} = 10V, I _D = 1A(2)
V _{GS(th)}	Gate Threshold Voltage	2.0	—	4.0	V	V _{DS} = V _{GS} , I _D = 250μA
I _{DSS}	Drain-to-Source Leakage Current	—	—	1	μA	V _{DS} = 85V, V _{GS} = 0V
I _{GSS}	Gate-Body Leakage Current	—	—	±100	nA	V _{DS} = 0V, V _{GS} = ±20V
T _J , T _{STG}	Operating and Storage Temperature	-55°C to 150°C Max.				

Mechanical Data

Die Drawing

Chip Size	3987 μm X 2012 μm
Gate Pad Size	379 μm X 540 μm
Source Pad Size	3568 μm X 1896 μm
Scribe Line Width	80 μm
Wafer Thickness	150 μm
Wafer Diameter	200 mm
Gross Die	3326 EA
Source Metallization	Al-Cu (6 μm typical)
Drain Metallization	Ti-Ni-Ag
Passivation	N/A
Recommended Storage Environment	Store in original container, in dry nitrogen, 6 months at ambient temperature of 23°C ± 3°C



(1) This characteristic assumes the die is assembled in TO-220 package. Actual performance may degrade when assembled.

(2) Pulse Width tp = < 300 μs, Duty Cycle < 2%.

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