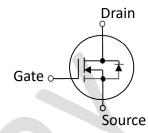




20V, 11A ⁽¹⁾ N-Channel MOSFET

Advanced Trench Device Design and Processes

- High Reliability Capability
- 100% CP Probing and Inking



SYMBOL

Electrical Characteristics in C/P Test (TJ at 25 °C)							
Symbol	Parameter	Min.	Тур.	Max.	Unit	Test Condition	
V _{(BR)DSS}	Drain-Source Breakdown Voltage	20	_	_	V	V _{GS} =0V, I _D =250µA	
R _{DS(ON)}	Static Drain-Source On-Resistance	—	_	7.2	mΩ	$V_{GS} = 4.5V, I_{D} = 1A(2)$	
R _{DS(ON)}	Static Drain-Source On-Resistance	_	1	10.2	mΩ	$V_{GS} = 2.5V, I_{D} = 1A(2)$	
$V_{GS(th)}$	Gate Threshold Voltage	0.5		1.5	V	V_{DS} = V_{GS} , I_D =250 μ A	
I _{DSS}	Drain-to-Source Leakage Current	—	—	1	μA	V_{DS} =20V, V_{GS} =0V	
I _{GSS}	Gate-Body Leakage Current	-	-	±100	nA	V_{DS} =0V, V_{GS} =±12V	
T _J , T _{STG}	Operating and Storage Temperature	-55°C to 150°C Max.					

Mechanical Data	Die Drawing	
Chip Size	1462 µm X 1420 µm	
Gate Pad Size	180 µm X 180 µm	
Source Pad Size	1234 µm X 631 µm	
Scribe Line Width	60 µm	
Wafer Thickness	150 µm	
Wafer Diameter	200 mm	
Gross Die	12827 EA	
Source Metallization	Al-Cu (4µm typical)	
Drain Metallization	Ti-Ni-Ag	
Passivation	N/A	
Recommended Storage Environment	Store in original container, in dry nitrogen, 6 months at ambient temperature of 23°C ± 3°C	

(1) This characteristic assumes the die is assembled in SO-8 package. Actual performance may degrade when assembled.

(2) Pulse Width tp = < 300 μ S, Duty Cycle < 2%.

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